

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,864,539 B2  
DATED : March 8, 2005  
INVENTOR(S) : Koichiro Ishibashi et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [75], Inventors, change "**Koichiro Ishibashi**, Warabi (JP)" to -- **Koichiro Ishibashi**, Warabi-shi (JP) --;

Item [56], **References Cited**, OTHER PUBLICATIONS, change

"S. Narendra et al., "1.IV IGHZ Communications Router with On-Chip Body Bias 150nm CMOS," ISSCC 2002/ Session 16/High Speed I/O 16.4, pp. 270, 271,466, Feb. 2002." to -- S. Narendra et al., "1.IV IGHZ Communications Router with On-Chip Body Bias 150nm CMOS," ISSCC 2002/ SESSION 16/HIGH SPEED I/O 16.4, pp. 270, 271,466, Feb. 2002 --.

Column 19,

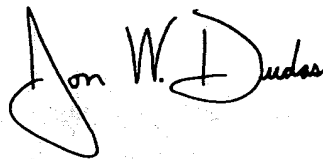
Line 36, after "MISFET" insert -- , --.

Column 20,

Line 21, change "cower" to -- power --.

Signed and Sealed this

Twenty-fifth Day of October, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*